***DELPHI***

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* Compiler & its Stages.
* Structure ---->Declaration, Padding, Method to Avoid padding using structuer declaration.
* Difference between Microcontroller & Microprocessor.
* Difference between soft & hard RTOS & soft & hard limit
* Flags of microcontroller & Processor.
* What all bits all set when overflaw conditions is reached?

* Realization of XOR using NAND.
* Counters & Modul counters.
* C=A+B ; - determine the possible errors.

* C=(A+B)/D ; - determine the possible errors.

* Difference between Latches & flip – flops.
* JK flip – flop. Race around condition & master – slave flip flop.
* Using JK design T & D flip – flop?
* Using Ternary operator to find (1) greatest of 2 nos

(2)greatest of 3 nos

* Scheduling process needs :

3 criteria ;- (1) priority of each process

(2) excution time

(3) time slice

* For (i=0, i<4; i++);

Printf (“%d”, i);

o/p :- ?

* Extern int i=10;

Printf(“%d”,i);

o/p :-?

or error?

* What is slow rate ?
* Binary to octal convention.
* What is overflow condition?
* Circuits;- Inverting & Nan- inverting configuration of OP-amp.
* Questions about FET & CMOS?
* What is power factor ?
* Aptitude ;-

work & time

percentage

profit & loss

ratio & proportion

related questions

* What are universal gate & why they are most popular?
* Alternative for an interpreter?

* Difference between compiler & interpreter?
* Why embedded domain? why Bangalore?

***INTERGRA MICRO***

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Project \_ Final year

Program to write.

HR questions as per the profile.